

PROGRAMME
Digital Signal Processing (DSP)
31 May – 9 June 2007, Sigtuna, Sweden

May 2007

Time	Friday 1 June	Saturday 2 June	Sunday 3 June	Monday 4 June	Tuesday 5 June	Wednesday 6 June	Thursday 7 June	Friday 8 June	Saturday 9 June	
08:30	Types of Accelerators and Specific Needs I	Math I	Math II	Math III	E X C U R S E S	Math IV	Real Time Control of Beam Parameters I	Real Time Control of Beam Parameters II	Buses to airport	
09:30	T. Shea	M. Hoffmann	M. Hoffmann	M. Hoffmann		M. Hoffmann	M. Dehler	M. Dehler		
09:30	Types of Accelerators and Specific Needs II	Control Theory I	Control Theory II	Control Theory III		RF Application I	RF Application II	Controls Integration II		
10:30	T. Shea	S. Simrock	S. Simrock	S. Simrock		T. Schilcher	T. Schilcher	T. Shea		
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11:00	High Level Modeling Tools I	Introduction to DSP I	Introduction to DSP II	Introduction to DSP III		Transverse Feedbacks	Controls Integration I	BI Application II		
12:00	J. Evans	M. Angoletta	M. Angoletta	M. Angoletta		M. Lonza	T. Shea	U. Raich		
12:00	High Level Modeling Tools II	Introduction to FPGA I	Introduction to FPGA II	Introduction to FPGA III		Longitudinal Feedbacks	BI Application I	Outlook or Seminar		
13:00	J. Evans	J. Serrano	J. Serrano	J. Serrano		M. Lonza	U. Raich	H. Schmickler		
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14:30	Introduction to Afternoon Courses	From Analog to Digital I	From Analog to Digital II	From Analog to Digital III	I O N	Parallel Lab course (DSP/FPGA) Group B/A	Parallel Lab course (DSP/FPGA) Group B/A	Parallel Lab course (DSP/FPGA) Group B/A		
15:30	H. Schmickler	J. Belleman	J. Belleman	J. Belleman		Parallel Lab course (DSP/FPGA) Group A/B	Parallel Lab course (DSP/FPGA) Group A/B	Discussion Session		
16:30	Parallel Lab course (DSP/FPGA) Group A/B	Parallel Lab course (DSP/FPGA) Group A/B	Parallel Lab course (DSP/FPGA) Group A/B	Parallel Lab course (DSP/FPGA) Group A/B		Parallel Lab course (DSP/FPGA) Group B/A	Parallel Lab course (DSP/FPGA) Group B/A	D. Brandt		
C O F F E E B R E A K					C O F F E E B R E A K					
17:00	Parallel Lab course (DSP/FPGA) Group A/B	Parallel Lab course (DSP/FPGA) Group A/B	Parallel Lab course (DSP/FPGA) Group A/B	Parallel Lab course (DSP/FPGA) Group B/A	Parallel Lab course (DSP/FPGA) Group B/A	Parallel Lab course (DSP/FPGA) Group B/A	Parallel Lab course (DSP/FPGA) Group B/A			
18:00	Parallel Lab course (DSP/FPGA) Group A/B	Parallel Lab course (DSP/FPGA) Group A/B	Parallel Lab course (DSP/FPGA) Group A/B	Parallel Lab course (DSP/FPGA) Group B/A	Parallel Lab course (DSP/FPGA) Group B/A	Parallel Lab course (DSP/FPGA) Group B/A	Parallel Lab course (DSP/FPGA) Group B/A			
19:00	Welcome Drink	DINNER	DINNER	DINNER	DINNER	DINNER	Special Dinner	DINNER		
19:30										