

Mathieu Saccani

- **Engineer in Electronics and Digital Communications** (Paris)
FPGA - Semiconductors - Mixed Signals - Digital Communications
- **Master Research in Radio-Telecommunication Systems** (Paris)
Telecommunication Theory - Coding - Wireless Communications - Networks
- **Digital Electronics Designer** (Paris)
Satellite On-Board Computers & Space Radio-Com Systems
- **ASIC-FPGA Design Team leader** (Paris/London)
Missile embedded On-Board Computers – FR/UK team
- **Digital Designer for BLM System (BE-BI-BL)**
New update of the LHC-BLM processing board (VFC-HD)
New update of the SPS and LHC BLM Acquisition Tunnel Board

